



PATENT
Docket No. 492322001810

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Nobuyuki SEKIWA *et al.*

Serial No.: 10/720,378

Filing Date: November 25, 2003

For: METHOD OF MANUFACTURING
INSULATED GATE
SEMICONDUCTOR DEVICE

Examiner: Shrinivas H. Rao

Group Art Unit: 2814

Conf. No.: 4282

Shrinivas H. Rao
-7/08/2006

AMENDMENT UNDER 37 CFR 1.116

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Action mailed August 9, 2005, please amend this application as follows:

The listing of claims begins on page 2.

The Remarks begin on page 5.